

## ***NEWS RELEASE***

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### ***Advantest Selects Calypto's Power Optimization and Verification Tools to Reduce Power in ASIC Designs***

*PowerPro<sup>®</sup> CG and SLEC<sup>®</sup> Pro reduce dynamic power; improve design turnaround time for ASICs*

**SANTA CLARA, Calif. — August 30, 2010** — Calypto<sup>®</sup> Design Systems Inc., the leader in sequential analysis technology, announced today that Advantest Corporation, a premier supplier of semiconductor test equipment, has adopted its PowerPro<sup>®</sup> CG (clock gating) and SLEC<sup>®</sup> Pro products to optimize power in its ASIC designs. Calypto offers the industry's leading RTL power optimization and formal verification solutions, enabling significant ASIC power reduction while shortening design time and improving design quality.

“Our ASIC design teams are under extreme pressure to deliver low power solutions, and we are very impressed with the outstanding results we have seen from PowerPro CG and SLEC Pro in our design flow,” said Mr. Hiroshi Kobayashi, general manager of Design Management Division, Advantest Corporation. “They are the only tools in the industry that are based on sequential analysis and offer fully automated RTL power optimization with an integrated, comprehensive verification flow.”

Based on Calypto's patented sequential analysis technology, PowerPro CG evaluates circuit behavior across multiple clock cycles to identify sequential clock gating opportunities and creates the required sequential clock-gating enable logic to reduce design power. PowerPro CG

then generates new power-optimized RTL that is identical to the original RTL except for the surgical insertion of the sequential clock-gating enable logic.

SLEC Pro comprehensively verifies the power-optimized RTL generated by PowerPro. SLEC Pro is a formal, functional sequential logic equivalence checker that ensures functional equivalence between the original RTL design and the corresponding power-optimized RTL design for all possible input sequences.

“Advantest is committed to providing leading automatic test equipment for the semiconductor industry,” said Tom Sandoval, chief executive officer of Calypto. “PowerPro CG and SLEC Pro will enable Advantest to incorporate advanced, ultra-low power ASICs into its products in the shortest possible time.”

#### **About PowerPro and SLEC Products**

The PowerPro product family is the industry’s most comprehensive RTL power optimization platform and includes PowerPro CG, PowerPro MG, PowerPro Analyzer and the PowerAdvisor Flow. The SLEC product family is a comprehensive formal verification product line that finds bugs that other tools miss. Based on Calypto’s patented sequential analysis technology, the SLEC product family includes the SLEC System, SLEC System-HLS, SLEC RTL and SLEC Pro.

#### **About Calypto**

Founded in 2002, Calypto Design Systems, Inc. empowers designers to create high-quality, low-power electronic systems by providing best-in-class power optimization and functional verification software, based on its patented sequential analysis technology. Calypto, whose customers include Fortune 500 companies worldwide, is a member of the Cadence Connections program, the IEEE-SA, Synopsys SystemVerilog Catalyst Program and the Mentor Graphics OpenDoor program. Calypto has offices in Europe, India, Japan and North America.

Corporate Headquarters is located at: 2933 Bunker Hill Lane, Suite 202, Santa Clara, Calif. 95054. Telephone: (408) 850-2300. More information can be found at: [www.calypto.com](http://www.calypto.com).

### **About Advantest**

A world-class technology company, Advantest is a leading producer of automatic test equipment (ATE) for the semiconductor industry and a premier manufacturer of measuring instruments used in the design and production of electronic instruments and systems. Its leading-edge systems and products are integrated into the most advanced semiconductor production lines in the world. The company also focuses on R&D for new markets that benefit from advancements in nanotech and terahertz technologies, and has recently introduced critical dimension scanning electron microscopes essential to photomask manufacturing, and a 3D imaging analysis tool for the auto industry. Founded in Tokyo in 1954, Advantest established its first subsidiary in 1982, in the USA, and now has subsidiaries worldwide. More information is available at [www.advantest.co.jp](http://www.advantest.co.jp)

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