

NEWS RELEASE

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Calypto Selects AST Ltd. as Distributor, Technical Representative in Israel *Strong Demand for Calypto Products Drives Move into Israel*

SANTA CLARA, CALIF. — May 13, 2008 — Calypto™ Design Systems Inc., the leader in sequential analysis technology, announced today it has selected Advanced Semiconductor Technology Ltd. (AST) to be its exclusive distributor and technical representative in Israel.

Driving this move is a strong demand in Israel for Calypto's register transfer level (RTL) power optimization and functional verification products. AST of Raanana, Israel, was selected because of its deep technical expertise and successful track record in the electronic design automation (EDA) market.

"AST has an excellent reputation and well-established relationships with many semiconductor companies," says Doug Aitelli, Calypto's vice president of worldwide sales. "AST will give Calypto an immediate, credible presence in Israel."

Calypto's advanced RTL power optimization and functional verification products, based on unique sequential analysis technology, dramatically reduce power and improve design quality. PowerPro™ CG (Clock Gating) is an automated RTL power optimization product that reduces power by up to 60%. Calypto's SLEC™ (Sequential Logic

Equivalence Checker) is the industry's only proven sequential equivalence checker capable of comparing designs with differences in timing, state and levels of abstraction.

“Calypto's PowerPro CG product offers unique capabilities to address the challenge of RTL power optimization,” remarks Uri Farkash, Director of EDA Operations at AST. “We see tremendous opportunity for Calypto's products within Israel.”

About AST

Advanced Semiconductor Technologies (AST) was established in 1986 as the first independent Application Specific Integrated Circuits (ASIC) Design Center in Israel. It represents today a group of world-class companies in the areas of ASIC, Electronic Design Automation (EDA) and Application Specific Standard Product (ASSP). Its team of highly skilled sales and application engineers combine more than 100 years of experience. AST provides turn-key services as well as onsite support and consulting, in the areas of ASIC and FPGA Design and Verification, EDA, Configuration Management (CM) and other Design and Development. For more information, visit: www.ast.co.il.

About Calypto

Founded in 2002, Calypto Design Systems, Inc. empowers designers to create high-quality, low-power electronic systems by providing best-in-class power optimization and functional verification software, based on its patented sequential analysis technology. Calypto, whose customers include Fortune 500 companies worldwide, is a member of the Cadence Connections program, the IEEE-SA, Synopsys SystemVerilog Catalyst Program and the Mentor Graphics OpenDoor program. Calypto has offices in Europe, India, Japan and North America. Corporate Headquarters is

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(408) 850-2300. More information can be found at: www.calypto.com.

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