

## ***NEWS RELEASE***

### **Calypto Design Systems Expands Presence in Japan**

*Company opens Japan office, adds representation to its technical advisory board*

**TOKYO, Japan, February 1, 2005** - Calypto Design Systems, Inc. today announced an expanded presence in the Japanese market. The company has attracted electronic design automation (EDA) industry veteran, Eiki Suzuki, to serve as president of its newly established subsidiary - Calypto Design Systems, K.K.. In addition, Calypto has also added Professor Masahiro Fujita of Tokyo University to its technical advisory board.

"Because Japanese design teams have been some of the early adopters of system-level design, there is a strong need in this market for technologies that bridge system and RTL design," stated Devadas Varma, CEO of Calypto. "We believe that our technology will have substantial impact on this market. Therefore, we are investing in the capabilities to support this market fully - both by adding the right people and developing the right products. Professor Fujita's technology direction and Mr. Suzuki's extensive business experience will be very valuable as we move forward."

Eiki Suzuki will lead Calypto K.K., as its president. Mr. Suzuki has more than 25 years of leadership and distribution experience in the EDA and Semiconductor industries. Prior to joining Calypto, he held executive roles at both the Cadence and Get2Chip after he successfully grew Get2Chip's business in Japan. Previously, he held CEO positions at both the SC Hightech

and Sumisho Electronic Devices distribution companies, where he helped grow the Japanese presence for multiple successful companies including, Gateway Design Automation, Synopsys, Inc, and Magma Design Automation.

Dr. Fujita brings extensive and valuable methodology experience to Calypto's advisory board. As a professor in the VLSI Design and Education Center (VDEC) at The University of Tokyo, his research interests include synthesis and verification at higher level design stages, hardware/software co-design and also digital/analog co-design. He has already contributed many innovations to digital design verification, synthesis and test. Prior to joining the University of Tokyo, he spent 15 years at Fujitsu where for the last six years he served as the director of CAD for Fujitsu Laboratories of America.

## **About Calypto**

Founded in 2002, Calypto Design Systems enables IC design teams to bridge the system-to-RTL design gap, thereby saving millions of dollars in design costs and silicon re-spins. The company delivers software products to leading edge semiconductor and systems companies worldwide. Calypto is privately held, with venture funding from Infineon Ventures, JAFCO Ventures, Tallwood Venture Capital and Walden International. The company is a member of the Cadence Connections program, IEEE-SA, the Open SystemC Initiative (OSCI), Synopsys SystemVerilog Catalyst Program, and has an ongoing alliance with the Model Technologies group of Mentor Graphics. More information about the company may be found at [www.calypto.com](http://www.calypto.com).

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