



## **NEWS RELEASE**

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### **Calypto's PowerPro MG Named 'Best of 2009' by Electronic Design**

SANTA CLARA, Calif. – December 16, 2009 – Calypto® Design Systems, the leader in sequential analysis technology, today announced that its PowerPro MG product has been chosen by *Electronic Design* magazine as one of the best products of 2009. The magazine publishes its “Best Electronic Design” issue annually. Staff editors choose the best products, technologies, and standards of the past year. [PowerPro MG](#) was one of only three EDA products selected for 2009.

“Calypto’s PowerPro MG is a truly unique solution to a very serious challenge in SoC design,” said David Maliniak, technology editor, *Electronic Design*. “While it is always difficult to single out a few products for the entire year, PowerPro MG, with its ability to dramatically reduce static and dynamic memory power consumption, is one of the most impressive products of 2009.”

PowerPro MG is the industry’s first product that automatically generates power-optimized RTL by taking advantage of the low-power modes available in today’s leading on-chip memories. Employing a new “[memory gating](#)” technique, PowerPro MG eliminates costly and time consuming manual coding. PowerPro MG debuted at this year’s Design Automation Conference (DAC) in San Francisco.

“Since we launched PowerPro MG in June, response industry-wide has been tremendous,” said Tom Sandoval, CEO of Calypto Design. “With memory accounting for up to 70 percent of power consumption in today’s designs, automating memory power optimization is critical for our customers. We are thrilled that *Electronic Design* recognizes the value of our tool for SoC designers and has chosen it as a Best of 2009.”

PowerPro MG applies sequential analysis techniques to automatically generate memory gating logic that significantly reduces both static and dynamic on-chip memory power. This provides a distinct



advantage for large, complex SoC designs in a variety of end applications such as wireless, storage, networking, graphics and multimedia.

### **About Calypto**

Founded in 2002, Calypto Design Systems, Inc. empowers designers to create high-quality, low-power electronic systems by providing best-in-class power optimization and functional verification software, based on its patented sequential analysis technology. Calypto, whose customers include Fortune 500 companies worldwide, is a member of the Cadence Connections program, the IEEE-SA, Synopsys SystemVerilog Catalyst Program, the Mentor Graphics OpenDoor program, Si2 and is an active participant in the Power Forward Initiative. Calypto has offices in Europe, India, Japan and North America. Corporate Headquarters is located at: 2933 Bunker Hill Lane, Suite 202, Santa Clara, Calif. 95054. Telephone: (408) 850-2300. More information can be found at: [www.calypto.com](http://www.calypto.com).

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