

NEWS RELEASE

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Calypto, Forte Collaboration Results in Advanced SystemC Design Flow *System-Level Design, Verification Solution Provides Competitive Advantage for Consumer, Multimedia SoC Applications*

SANTA CLARA, CALIF. — October 22, 2008 — As a result of more than three years of collaboration, Calypto™ Design Systems Inc. and Forte Design Systems have created an advanced SystemC design flow encompassing verification and implementation that provides consumer and multimedia electronics companies with improved productivity and design quality.

The seamless integration of Calypto's SLECT™ System-HLS formal verification software and Forte's Cynthesizer™ SystemC synthesis offers a complete SystemC to register transfer level (RTL) design flow. This advanced system-level solution has been proven by one of Japan's largest integrated device manufacturers (IDM) to support the complexity and capacity requirements of multi-million gate system-on-chip (SoC) designs.

Both products have been extensively tuned on numerous SystemC designs to satisfy sophisticated user requirements, such as custom interface specifications, external

memory interfaces and fixed point datatypes. The advanced SystemC design flow enables users to specify and verify SoC functionality, communication and timing at the system level. SLEC verifies the equivalence of two SystemC models during system-level model refinement and comprehensively verifies the RTL code generated from Cynthesizer.

“Japanese SOC design teams continue to break new ground in deploying SystemC design methods,” notes Tom Sandoval, Calypto’s chief executive officer (CEO). “The advanced SystemC design flow linking Calypto and Forte products is enabling industry leading design productivity in the development of complex consumer and multimedia SOCs.”

“High-level synthesis continues to provide a significant time-to-market advantage for design teams around the world,” says Sean Dart, Forte’s president and CEO.

“Coupled with sequential logic equivalence checking, designers can further improve their productivity and confidence using industry standard SystemC in their ESL design flow.”

Calypto, the leader in sequential analysis technology, will demonstrate this advanced SystemC solution at the Forte User Group Meeting in Shin Yokohama, Japan, Friday, October 31. For details, visit: www.ForteDS.co.jp

About SLEC

SLECTM System-HLS verifies the RTL generated from high-level synthesis (HLS) tools and is tightly integrated into HLS design flows by automating functional verification setup and supporting HLS language extensions. SLEC System-HLS verifies SystemC and RTL designs without the need for writing testbenches or running

simulation. SLEC System-HLS is part of Calypto's proven SLEC product family that also includes SLEC System, SLEC RTL and SLEC CG.

About Cynthesizer

Forte's Cynthesizer delivers production quality RTL in one tenth of the time of hand coded RTL designs, improves results, and eliminates downstream timing closure problems. Cynthesizer uses a high-level SystemC description to give designers the ability to build designs with custom interfaces and challenging architectural requirements not possibly with other C-based products. Cynthesizer can automatically retarget designs to new speeds and process technologies and supports a level of reuse that is impossible in RTL. Cynthesizer is the practical, silicon-proven, high-level synthesis solution.

About Calypto

Founded in 2002, Calypto Design Systems, Inc. empowers designers to create high-quality, low-power electronic systems by providing best-in-class power optimization and functional verification software, based on its patented sequential analysis technology. Calypto, whose customers include Fortune 500 companies worldwide, is a member of the Cadence Connections program, the IEEE-SA, Synopsys SystemVerilog Catalyst Program, the Mentor Graphics OpenDoor program, Si2 and is an active participant in the Power Forward Initiative. Calypto has offices in Europe, India, Japan and North America. Corporate Headquarters is located at: 2933 Bunker Hill Lane, Suite 202, Santa Clara, Calif. 95054. Telephone: (408) 850-2300. More information can be found at: www.calypto.com.

About Forte Design Systems

Forte Design Systems is a leading provider of software products that enable design at a higher-level of abstraction. Forte's innovative, high-level synthesis technology allows design teams creating complex electronic systems to significantly reduce their overall design and verification time. More than 25 of the world's largest systems and semiconductor companies have deployed Cynthesizer today for their next generation products. Forte is headquartered at 100 Century Center Court, San Jose, Calif., 95112. For more information, visit www.ForteDS.com.

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