

NEWS RELEASE

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Calypto Delivers 'Picture Perfect' ESL Solution to Casio

Calypto Teams with Cadence Design Systems to Improve Design Efficiency, Speed Time-to-Market of Digital Cameras

SANTA CLARA, Calif. – June 9, 2009 – Demonstrating the widespread adoption of its popular SLEC® System-HLS (High Level Synthesis) tool, Calypto® Design Systems, the leader in sequential analysis technology, today announced that Casio will incorporate SLEC into its electronic system level (ESL) design flow for new digital camera designs. Casio plans to pair Calypto's formal verification tool with the Cadence Design Systems, Inc. C-to-Silicon Compiler to improve design efficiency and reduce time-to-market for its digital cameras.

The combination of the Cadence and Calypto technologies allows engineers to work at a much higher level of abstraction, engaging in true ESL design. Using C-to-Silicon Compiler, Casio will automatically generate synthesizable register transfer level (RTL) code from SystemC code that can then be comprehensively verified by SLEC System-HLS. The automated ESL flow will allow Casio to run multiple "what if" scenarios and evaluate different design implementations to ensure that the final product delivers the best combination of cost and performance possible while ensuring functional correctness.

"Digital camera design has become increasingly sophisticated, and gate complexity is now 10 times what it was just three years ago," said Mr. Kazuyuki Kurosawa, section manager of Digital Camera

Product Unit, Casio. “Before implementing an automated unified ESL flow, we were forced to engage in time-consuming RTL hand coding and verification through RTL simulation regressions to generate and verify our design—a process that consumed valuable engineering resources and limited our ability to innovate. By automating these two complex steps, Calypto and Cadence allow us to reduce design time and focus our resources on bringing true differentiation to our products.”

“There is an ongoing effort for design teams to find ways to increase productivity and achieve faster time-to-market,” said Tom Sandoval, CEO Calypto Design Systems. “Cadence C-to-Silicon Compiler and its integration with Calypto’s SLEC System-HLS are proven to meet this demand, allowing designers to use their valuable design time for innovation, rather than tedious simulation and coding.”

Cadence, Calypto Combination Deliver Streamlined ESL Flow for SoC Designs

Cadence C-to-Silicon Compiler bridges the gap between RTL design and system-level models, usually written in C/C++ and System-C. Fulfilling the industry requirement for formal equivalence checking, designers can then perform comprehensive functional verification using the Calypto SLEC System-HLS to formally verify equivalence between SystemC ESL models and RTL implementations. By integrating their technologies, Cadence and Calypto deliver a fully automated system-level design solution that dramatically increases designer productivity.

“Our customers now have a best-in-class comprehensive solution for system level design and verification,” said Michael McNamara, vice president and general manager of the Systems Software Group at Cadence Design Systems. “The tight integration between Calypto’s SLEC System-HLS and Cadence C-to-Silicon Compiler gives our customers an optimal flow that is rapidly becoming a standard approach for advanced SoC design.”

About Calypto

Founded in 2002, Calypto Design Systems, Inc. empowers designers to create high-quality, low-power electronic systems by providing best-in-class power optimization and functional verification

software, based on its patented sequential analysis technology. Calypto, whose customers include Fortune 500 companies worldwide, is a member of the Cadence Connections program, the IEEE-SA, Synopsys SystemVerilog Catalyst Program, the Mentor Graphics OpenDoor program, Si2 and is an active participant in the Power Forward Initiative. Calypto has offices in Europe, India, Japan and North America. Corporate Headquarters is located at: 2933 Bunker Hill Lane, Suite 202, Santa Clara, Calif. 95054. Telephone: (408) 850-2300. More information can be found at: www.calypto.com.

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