

# ***NEWS RELEASE***

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## **Calypto Names Innotech its Exclusive Distributor in Japan**

*Extends Calypto's Reach in the Global Electronics Market*

**SANTA CLARA, CALIF. — April 26, 2010** — Calypto<sup>®</sup> Design Systems Inc., the leader in sequential analysis technology, has selected Innotech Corporation as the exclusive distributor of its power optimization and functional verification tools in Japan. Representing the world's leading semiconductor and EDA products, Innotech will now provide sales and technical support for Calypto's award-winning PowerPro<sup>®</sup> and SLEC<sup>®</sup> (Sequential Logic Equivalence Checker) product lines.

“Partnering with Innotech allows us to extend our reach to a broad set of customers within the growing design community in Japan,” said Tom Sandoval, Calypto's chief executive officer. “Innotech's outstanding reputation and expertise in cutting-edge EDA solutions strengthens Calypto's position in the Japan market as the leading provider of power optimization and functional verification tools for complex SoC design.”

Calypto's advanced register transfer level (RTL) power optimization and functional verification products, based on unique sequential analysis technology, dramatically reduce power and improve design quality. PowerPro CG is an automated

RTL power optimization product that reduces power by up to 60 percent. PowerPro MG is the industry's only automated memory power optimization solution that takes advantage of the low-power control options available in today's on-chip memories to reduce both dynamic and leakage memory power with little or no impact to timing or area. Calypto's SLEC is the industry's only proven sequential equivalence checker capable of verifying RTL designs despite differences in timing, state and levels of abstraction.

“Innotech is committed to providing the most productive and efficient design solutions to the Japanese market,” said Takashi Takahashi, director of Innotech. “Adding Calypto's PowerPro and SLEC products allows us to fill critical gaps in our highly competitive EDA product portfolio. Together with Calypto, we now provide the most advanced design flow for our SoC customers.”

### **About Innotech**

Founded in 1987, Innotech is Japan's premier importer of high-end semiconductor devices and equipment and a skilled support partner to many of Japan's top electronics makers. The Company maintains direct relationships with every major semiconductor manufacturer in Japan. For more information, visit [www.innotech.co.jp](http://www.innotech.co.jp).

### **About Calypto**

Founded in 2002, Calypto Design Systems, Inc. empowers designers to create high-quality, low-power electronic systems by providing best-in-class power optimization and functional verification software, based on its patented sequential analysis technology. Calypto, whose customers include Fortune 500 companies worldwide, is a member of the Cadence Connections program, the IEEE-SA, Synopsys

SystemVerilog Catalyst program and the Mentor Graphics OpenDoor program. Calypto has offices in Europe, India, Japan and North America. Corporate headquarters is located at: 2933 Bunker Hill Lane, Suite 202, Santa Clara, Calif. 95054. Telephone: (408) 850-2300. More information can be found at: [www.calypto.com](http://www.calypto.com).

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