

NEWS RELEASE



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Calypto's PowerPro MG Named a Finalist in 20th Annual EDN Innovation Awards

*A prestigious honor for the industry's only automated memory
power optimization tool*

SANTA CLARA, CALIF. — February 22, 2010 — Calypto[®] Design Systems, the leader in sequential analysis technology, announced that *EDN Magazine* has selected its [PowerPro[®] MG](#) (memory gating) tool as an Innovation Award finalist in the “EDA: Front-End Analysis and Synthesis Tools category.” The awards honor the people, products, and technologies that have shaped the semiconductor industry over the past year.

EDN readers select winners by casting votes for their favorite finalists during the months of February and March. The magazine's editorial staff also takes part in determining the final winners. Online balloting and nominee information can be found by visiting www.EDN.com/innovation20. Winners will be announced at a reception and awards ceremony on April 26, 2010, in San Jose, Calif.

“We received an impressive number of submissions for our 2009 Innovation Awards program, indicating that innovation is alive and well despite the economically challenging year,” said Rick Nelson, EDN editor-in-chief. “Now, it's up to the readers to determine which product in each category they find most compelling.”

About PowerPro MG

PowerPro MG is the industry's only EDA tool capable of automatically optimizing memory power in an RTL design. PowerPro MG reads in a VHDL or Verilog design as well as the applicable memory models. Using Calypto's patented sequential analysis technology, the tool generates new memory gating logic that works in conjunction with the low-power memory modes available in today's on-chip memories to

produce the lowest power memory implementation possible. PowerPro MG then generates new power-optimized RTL that looks identical to the original RTL except for the addition of the new memory gating logic. Aimed at large, complex SoC designs, PowerPro MG has been proven to reduce both dynamic and leakage memory power by up to 80 percent in a variety of end applications such as storage, networking, wireless, and multimedia.

About EDN and EDN.com

EDN serves the vital information needs of design engineers and engineering managers worldwide. EDN.com delivers a three-dimensional view of the electronic industry via news coverage, strategic business information, and in-depth technical content (www.edn.com). EDN is published by Reed Business Information (www.reedbusiness.com/us), the largest business-to-business publisher in the United States and a member of the Reed Elsevier Group Plc (NYSE: RUK and ENL) – a world-leading publisher and information provider.

About Calypto

Founded in 2002, Calypto[®] Design Systems, Inc. empowers designers to create high-quality, low-power electronic systems by providing best-in-class power optimization and functional verification software, based on its patented sequential analysis technology. Calypto, whose customers include Fortune 500 companies worldwide, is a member of the Cadence Connections program, the IEEE-SA, Synopsys SystemVerilog Catalyst Program, the Mentor Graphics OpenDoor program, Si2 and is an active participant in the Power Forward Initiative. Calypto has offices in Europe, India, Japan and North America. Corporate headquarters is located at: 2933 Bunker Hill Lane, Suite 202, Santa Clara, Calif. 95054. Telephone: (408) 850-2300. More information can be found at: www.calypto.com.

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